

Lead Circuit Designer / Director Circuit Design

- ▶ Experience with high-speed DDR / GDDR IO and PHY design including ESD, slew-rate control, impedance control, noise, jitter, SSO and other circuit and system-level issues.
- ▶ Must have analog circuit-level knowledge and experience.
- ▶ Experience with DLL/PLL design and use.
- ▶ Must have supervised circuit designers and circuit design projects from conception to high-volume production.
- ▶ Must have ability to independently manage and organize IO and PHY circuit design from specification, through simulation, layout, design reviews, bring-up and production.
- ▶ Candidate will likely have a PhD or MS in EE and have more than 10 years of experience in circuit design.