



## **Verification Engineer**

As a verification engineer in a small start up environment you will be involved with ASIC and system level verification as well as in product bring up

- Strong verification background, with some design experience being a plus
- Experience with memory controllers and memory subsystems desired
- Strong programming skills in VERILOG, C and PERL, comfort with a diversity of programming environments and challenges
- Candidate will likely have MS in EE with 6 or more years of relevant experience

### **Required skills include:**

- Strong verification background, with some design experience being a plus
- Experience with memory controllers and memory subsystems desired
- Strong programming skills in VERILOG, C and PERL, comfort with a diversity of programming environments and challenges
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### **The following would be a plus**

- Basic familiarity with lab test equipment such as oscilloscopes and logic analyzers
- Experience with ATPG, fault simulations and test vector development
- Experience with bring up environment and software tools e.g. logic analyzer, ATE, JTAG, BIOS, etc.

### **MSEE preferred**

- Candidate will likely have MS in EE with 6 or more years of relevant experience